



Product Summary

September 2011

Zaltys IP Cores					
HDRM-M	High data rate modulator supporting BSPK, QPSK, OQPSK, 8/16/32/64/128/256 QAM, 16/32 APSK and more				
DVBS2-M	DVB-S2 compliant modulator supporting QPSK, 8PSK, 16/32 APSK, pilot tones and VCM				
HDRM-D	High data rate demodulator supporting BPSK, QPSK, OQPSK, 8PSK and 16QAM				
HDRM-D2	High resolution variant of HDRM-D with the addition of 8/32/64 QAM, 16/32 APSK & blind adaptive equalisation				
DVBS2-D	DVB-S2 compliant demodulator supporting QPSK, 8PSK, 16/32 APSK, with equaliser and VCM				
ISDBT-M	Full 13-segment hierarchical ISDB-T modulator with BTS interface compliant with ARIB STD-B31				
ISDBT-D	Full 13-segment hierarchical ISDB-T demodulator compliant with ARIB STD-B31				
MPE-E	MPE encoder which converts a datastream (e.g. Ethernet) to an MPE standard formatted transport stream				
MPE-D	MPE decoder which converts an MPE standard formatted transport stream to a datastream (e.g. Ethernet)				
DVBS-FE	Forward error correction (FEC) encoder compliant with DVB Digital Satellite News Gathering (DSNG) standard				
DVBS-FD	Forward error correction (FEC) decoder compliant with DVB Digital Satellite News Gathering (DSNG) standard				
FANO-FED	General purpose convolutional forward error correction (FEC) codec based on the Fano algorithm				

Zaltys Ev	aluation Boards
ZMP-001	Expandable modulator/demodulator evaluation board. Based on a Xilinx Virtex-4 FPGA with on board dual ADC & DAC (for I/Q input and output), variable gain amplifiers, L-band tuner, CPU, plentiful I/O, flexible clock generation, USB and RS-232 interfaces.
ZNZ-001	Advanced expandable modulator/demodulator evaluation board. Based on two Xilinx FPGAs with on board dual 16-bit ADC & DAC (for I/Q input and output), RF modulator and demodulator, variable gain amplifiers, DDR memory, ARM Cortex CPU, plentiful I/O, three10/100/1000Mbps Ethernet interfaces, flexible clock generation, USB and RS-232 interfaces, and two VITA 57.1 FMC connectors for expansion and connection of 3 rd party modules. (Available Q1 2012)

Zaltys Evaluation Software					
ZEDwire	PC software running under Microsoft Windows®. It interacts with Zaltys evaluation boards enabling rapid				
Communicator	loading, testing and evaluation of Zaltys IP cores.				



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ZEDwire Communicator software

About Silicon Infusion and Zaltys

Silicon Infusion has been established for over ten years, and has a successful history of providing unique and innovative technical solutions to the wireless telecommunications industry. Our global client list includes organisations from many diverse market sectors - from Broadcast and Telecoms equipment manufacturers to Military solutions providers.

The Zaltys range of products are used for efficient highspeed transmission of voice, video and data. Zaltys modem cores are currently being used in many third-party products, carrying many thousands of user connections on a daily basis.

Customised Solutions

All our Off-the-Shelf products can be tailored to suit individual customer requirements, such as the addition of specific microprocessor interfaces, or resource optimisation to fit specific FPGA devices.

Where there is a requirement for a new core that is outside our existing portfolio, we are happy to consider custom development work. Please contact us to discuss your requirements.



ZMP-001 Evaluation Board



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